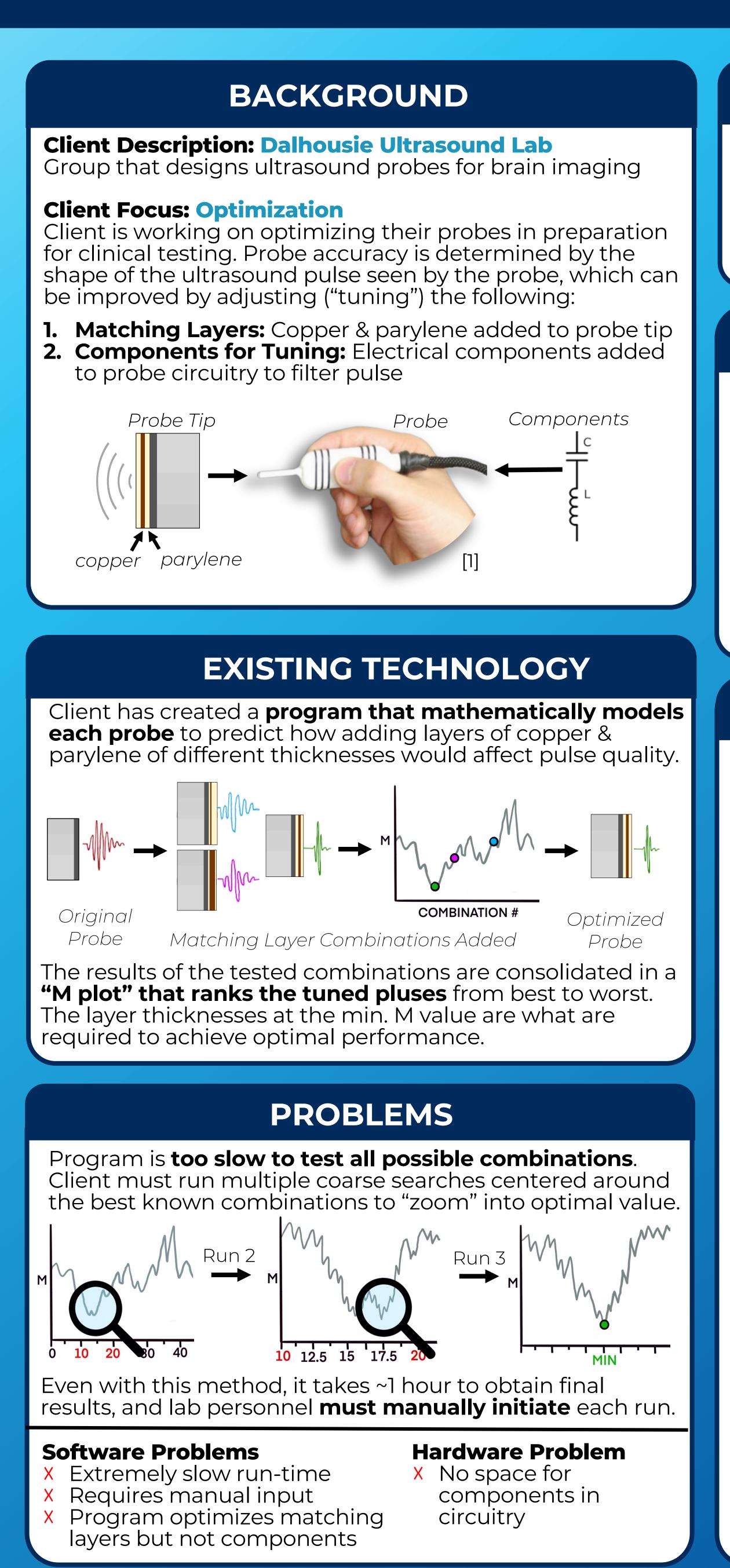


DALHOUSIE UNIVERSITY

FACULTY OF ENGINEERING

Annika Benson

Department of Electrical & Computer Engineering









Design, Fabrication & Testing of Optimized Electrical & Acoustic Tuning in an Ultrasound Endoscope

NEW DESIGN REQUIREMENTS

Software Needs

Decrease run-time and/or human hours needed

Jemma Pryde

Update program to determine optimal tuning components to achieve target of 66% bandwidth across 20-40MHz range

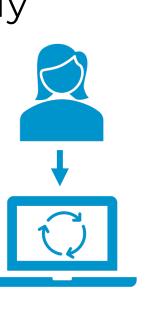
FIXING SPEED: SOFTWARE DESIGNS CONSIDERED

Alternative 1: Automated Iteration

Modify software to automatically identify most promising combinations & re-run optimization (rather than manually).

- Can build on existing code
- Eliminates need for human input
- Minimal run-time improvement
- Possible to miss some desirable
- combinations

NOT SELECTED



 Drastic run-time improvement Evaluates all combinations Eliminates need for human input X Significant modifications to code

SELECTED

FINAL DESIGN

Software: Parallelizing

A portion of the program was re-written to make used of parfor loops – a MATLAB function that takes looped section of code and divides the iterations amongst multiple processors.

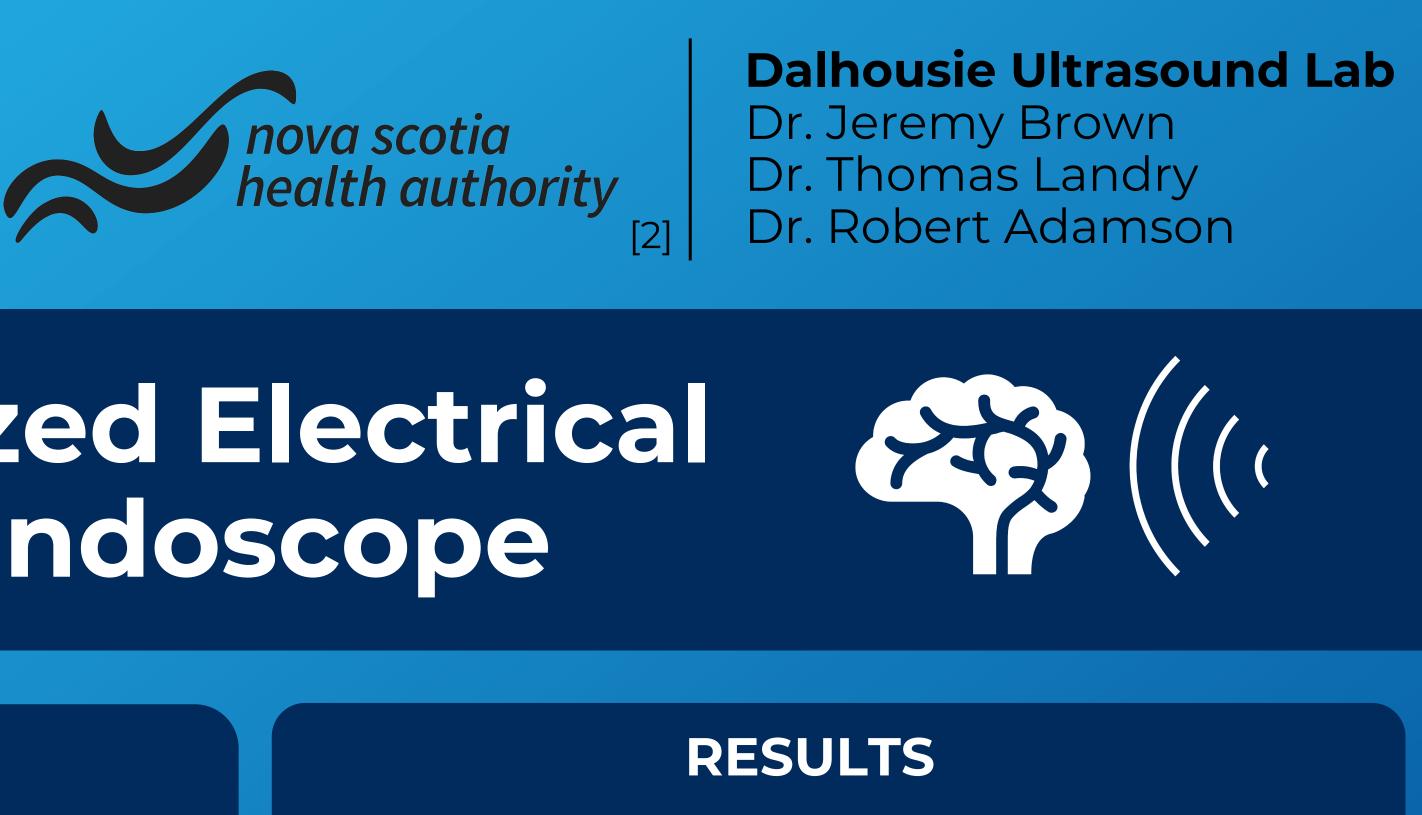
Software: Added Tuning Component Optimization

Expanded probe modeling software to predict how adding different component values/configurations would affect pulse quality.

	Design Details		Rationale
	Only test pairs of 2 components	•	Satisfies limited hardware spa Fewer combinations = faster p
(Only test standard, commercially-available component values	•	No time wasted on infeasible of Consistent hardware footprint PCB design can be re-used)
7	of 9 possible component configurations tested	•	Eliminates configs. with poor tabilities according to literature

Hardware: PCB Re-Design

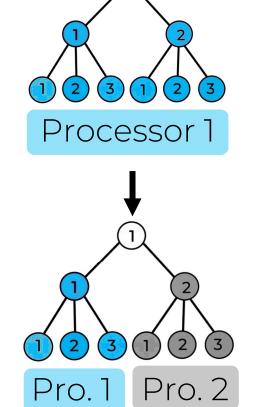
	Prior	After	Rationa
Breakout PCB: Tuning Components	Don't fit	Components fit in series-shunt configuration	 Optimal con according to modeling so
Breakout PCB: Layers	2	4	 Components mounted on Original boa retained
Transmit PCB: Layers	8	4	Cheaper to p



Hardware Needs

Make room for and add tuning components on "Breakout" PCB Reduce "Transmit" PCB from 8 to 4 layers

Alternative 2: Parallel Processing Combination-testing divided between computer's multiple processors which work simultaneously to obtain results. Only one run is required.



Probe tip connector

New "Breakout PCB" Design

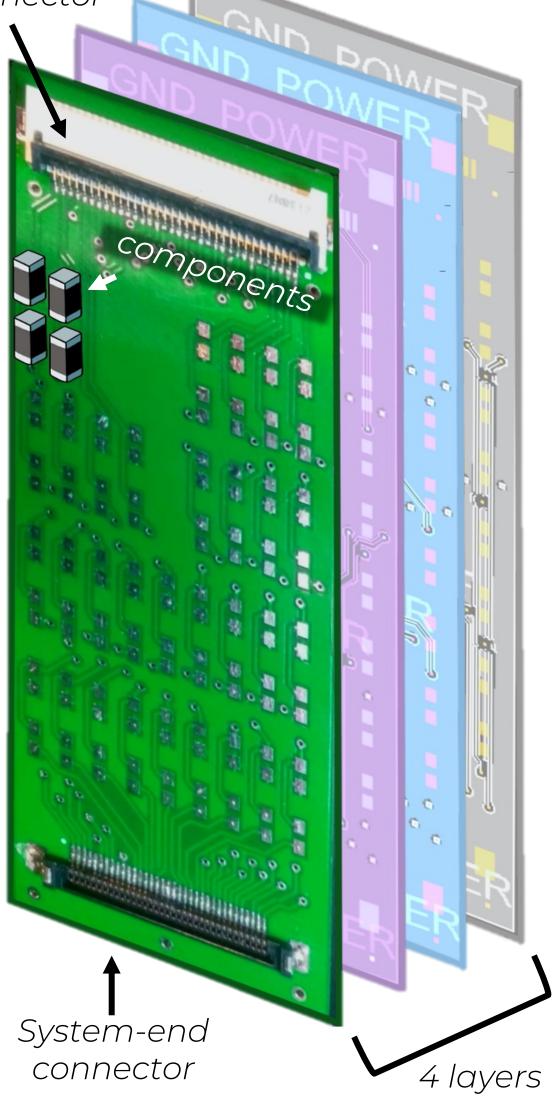
program options t (same

tuning

nfig.

oftware ts can be n topside ard width

produce

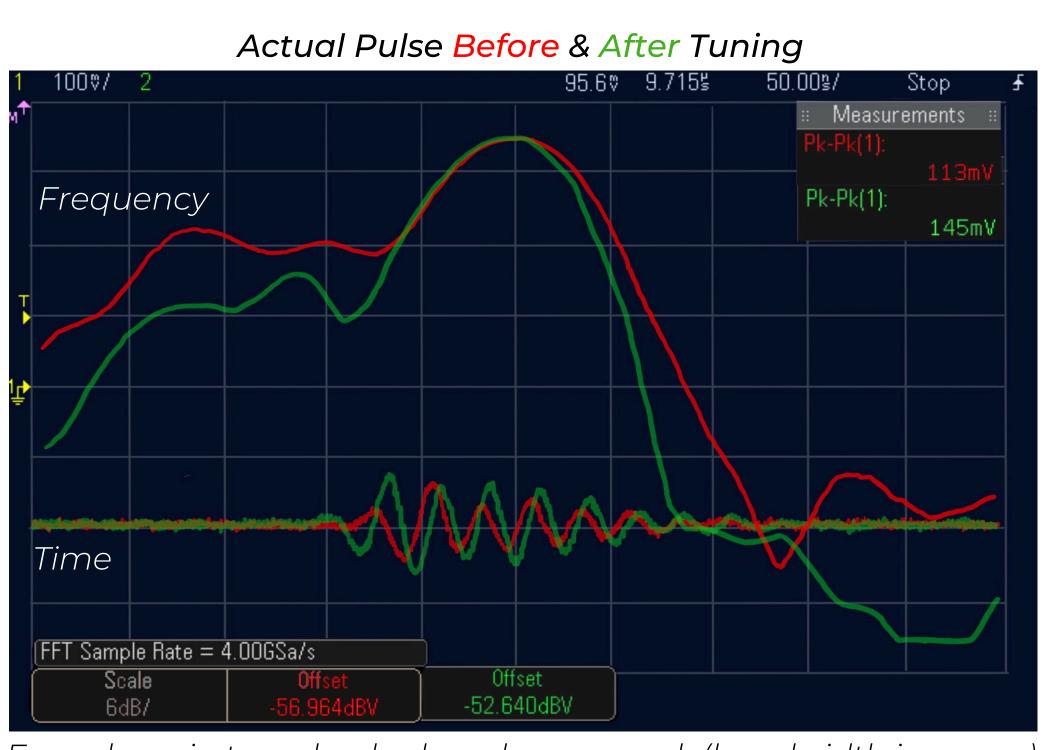


Efficiency

✓ Run-time decreased by 10%

Tuning Component Optimization

- Determined best tuning circuit configuration to be series-shunt inductors
- M-value decreased
- (improved) by up to 30%



Freq. domain tuned pulse has sharper peak (bandwidth increase) Time domain tuned pulse has higher amplitude (sensitivity increase)

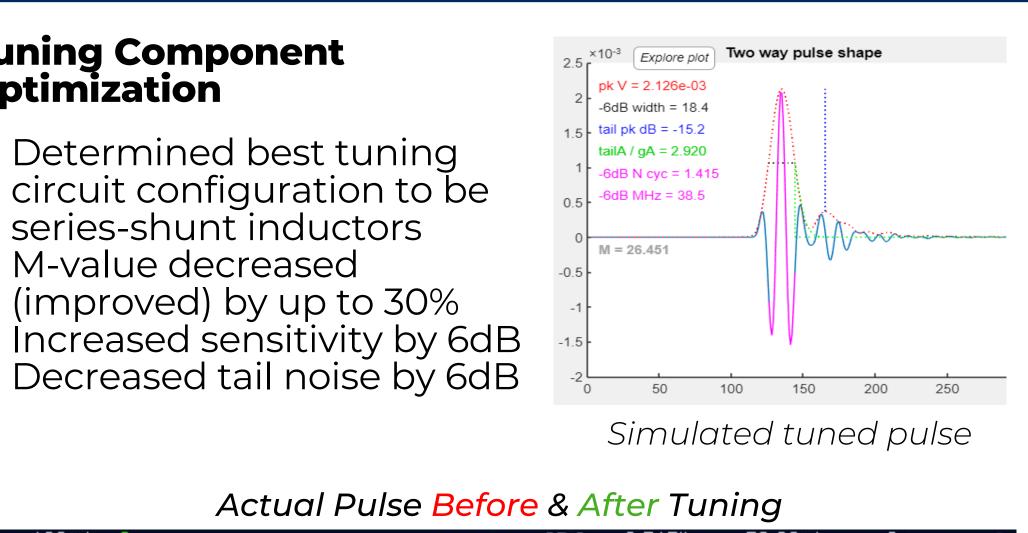
Hardware

Added tuning components to "Breakout" PCB ✓ Reduced "Transmit" PCB from 8 to 4 layers

Parallelization was only implemented for a portion of the program. For maximum run-time improvements, the remaining test loops should be parallelized. The team has provided the client with a detailed design report outlining code changes needed to achieve this. To further improve efficiency, remote processors (ex. Amazon Web Services) could be used for additional processing power.

[1] Dalhousie Ultrasound Lab. (2018, February 28). Opportunities. Retrieved March 25, 2021, from https://dalhousieultrasound.com/opportunities/

[2] Nova Scotia Health Authority. (n.d.). Nova Scotia Health Authority Logos. Retrieved March 24, 2021, from http://www.nshealth.ca/files/nova-scotia-health-authority-logos



RECOMMENDATIONS

REFERENCES