

# US Anemometer: The Better Way to Measure Wind

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## Introduction

### Background & Significance:

Ultrasonic anemometers are devices used to measure wind direction and speed using ultrasonic sound waves. The lack of moving parts makes them appropriate for long-term use in harsh marine environments.

### Problem Statement

Most of the ultrasonic anemometers on the market today are extremely expensive. As a result, this makes them inaccessible to fishermen, researchers, hobbyist sailors, and dingy racers. The US Anemometer project is addressing these concerns through the development of the theory and a prototype for a robust and low-cost anemometer, suitable for use on various sized boats.

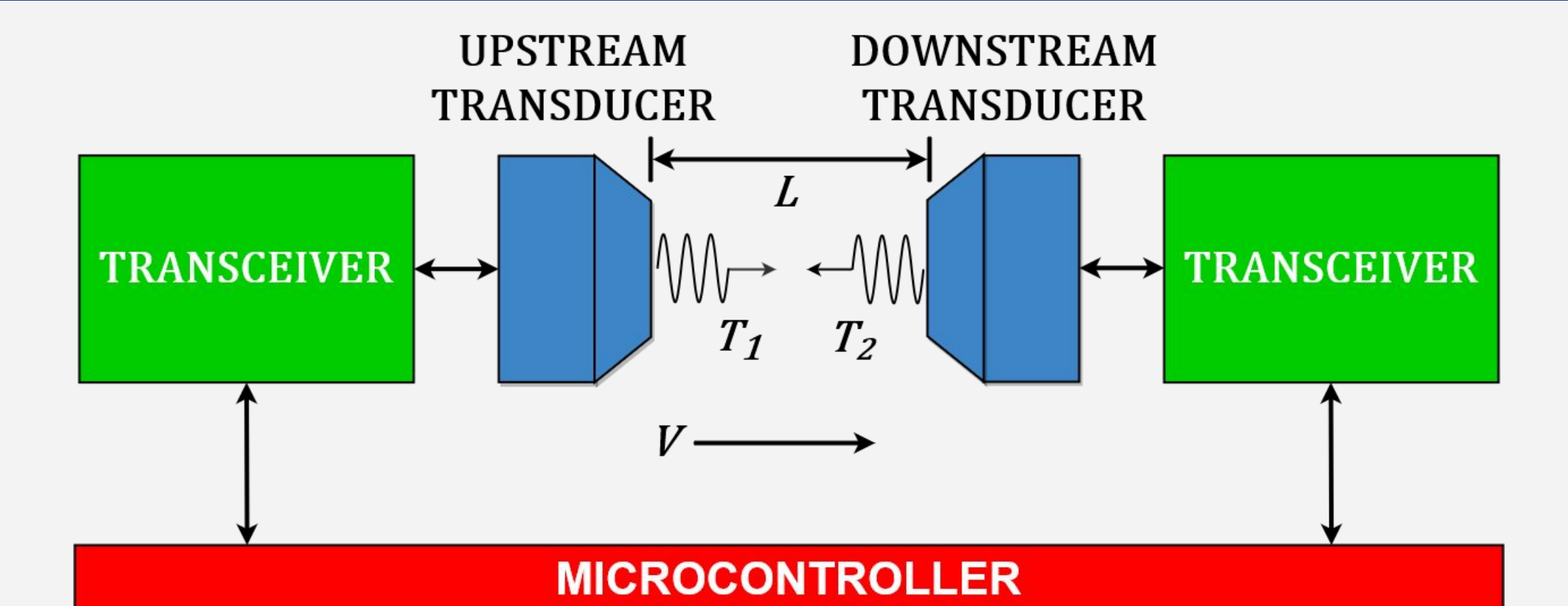
### Design Requirements:

- **Wind Speed:** The wind speed relative to the boat must be known to an accuracy of  $\pm 5\%$  (correctness plus precision) over an operating range from 0 m/s to 30 m/s.
- **Wind Direction:** The wind direction relative to the boat must be measured to an accuracy of  $\pm 3^\circ$  (correctness plus precision) over an operating range from 0 m/s to 30 m/s.
- **Output Rate:** The device must provide wind measurements at a minimum output frequency of 2 Hz.

### Project Deliverable:

A prototype that will measure wind speed along a single axis. It will encompass a combination of concepts used to achieve the required functionality of the proposed system with the necessary level of performance needed to meet the project specifications.

## Theory of Operation



UPSTREAM TIME OF FLIGHT:  $T_1$       DOWNSTREAM TIME OF FLIGHT:  $T_2$

$$T_1 = \frac{L}{C + V} \qquad T_2 = \frac{L}{C - V}$$

$$\text{WIND SPEED: } V = \frac{L}{2} \left( \frac{1}{T_1} - \frac{1}{T_2} \right)$$

## Hardware Design

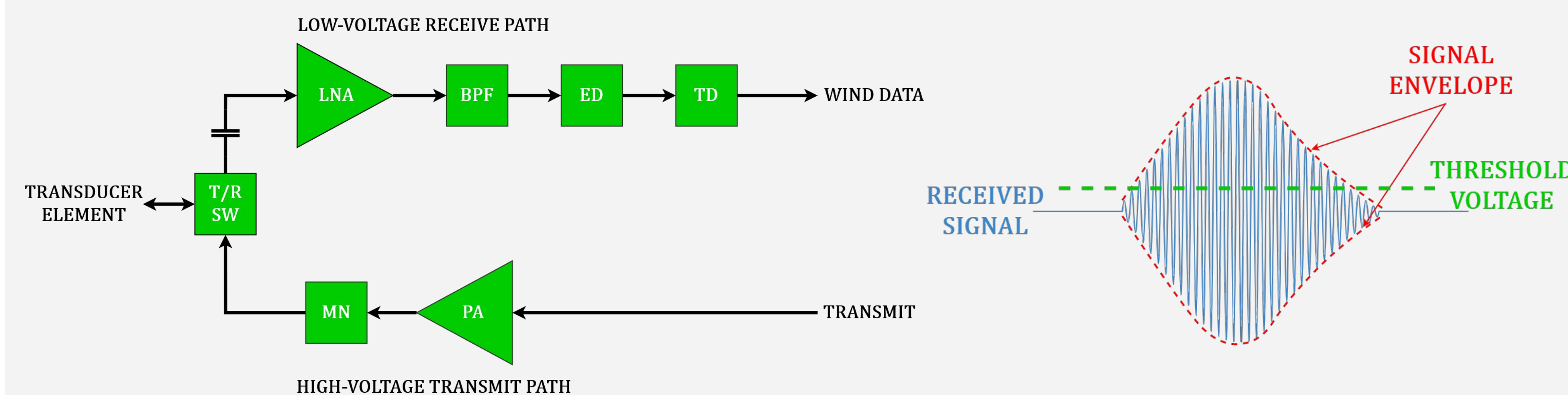
### High-Voltage Transmit Path:

To produce a measurement of the speed of the wind, a microcontroller generates low-voltage pulses to drive the transceiver's high-voltage Power Amplifier (PA) and excite the transducer element. A Matching Network (MN) is used to maximize power transfer to the transducer element.

### Low-Voltage Receive Path:

The receiver consists of a Transmit/Receive (T/R) Switch, a Low-Noise Amplifier (LNA), a Bandpass Filter (BPF), an Envelope Detector (ED), and a Threshold Detector (TD):

- Each transducer element is connected to an LNA through a T/R switch, which protects the LNA input from the high-voltage transmit signal.
- The LNA provides a fixed gain to optimize the receiver's noise performance.
- The bandpass filter removes noise contained in frequencies outside the band of interest (40 kHz).
- The envelope detector extracts the envelope of the received signal.
- The threshold detector converts the output of the envelope detector into a digital signal that can be read by the microcontroller.

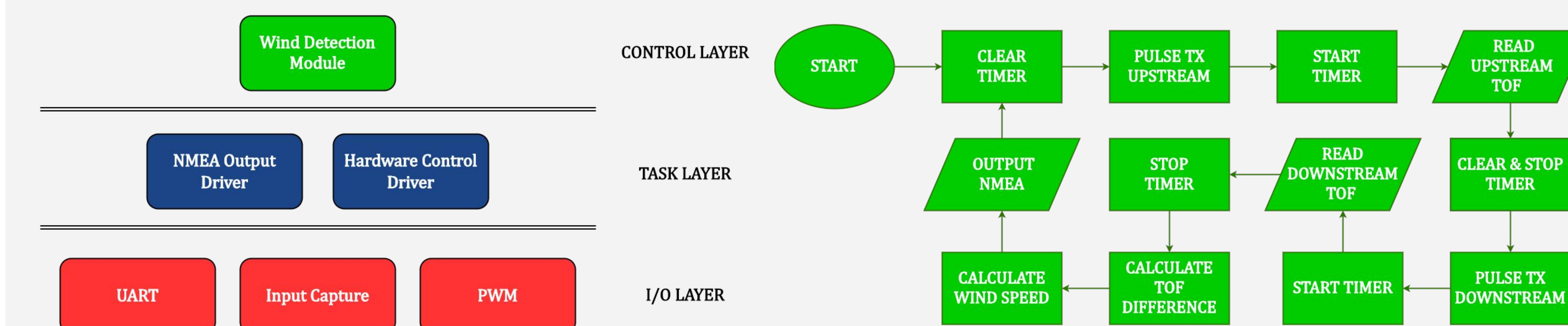


## Software Design

### Software Architecture:

The software of the device will be structured in a hierarchical architecture with 3 distinct layers: Control, Task, and I/O:

- Control Layer: Manages the state of the device and the tasks that are being performed
- Task Layer: Contains modules that will control the ultrasonic transducers for the device
- I/O Layer: Low-level drivers that manage the hardware for the various sensors and amplifiers in use



## Future Work

### Hardware Tasks

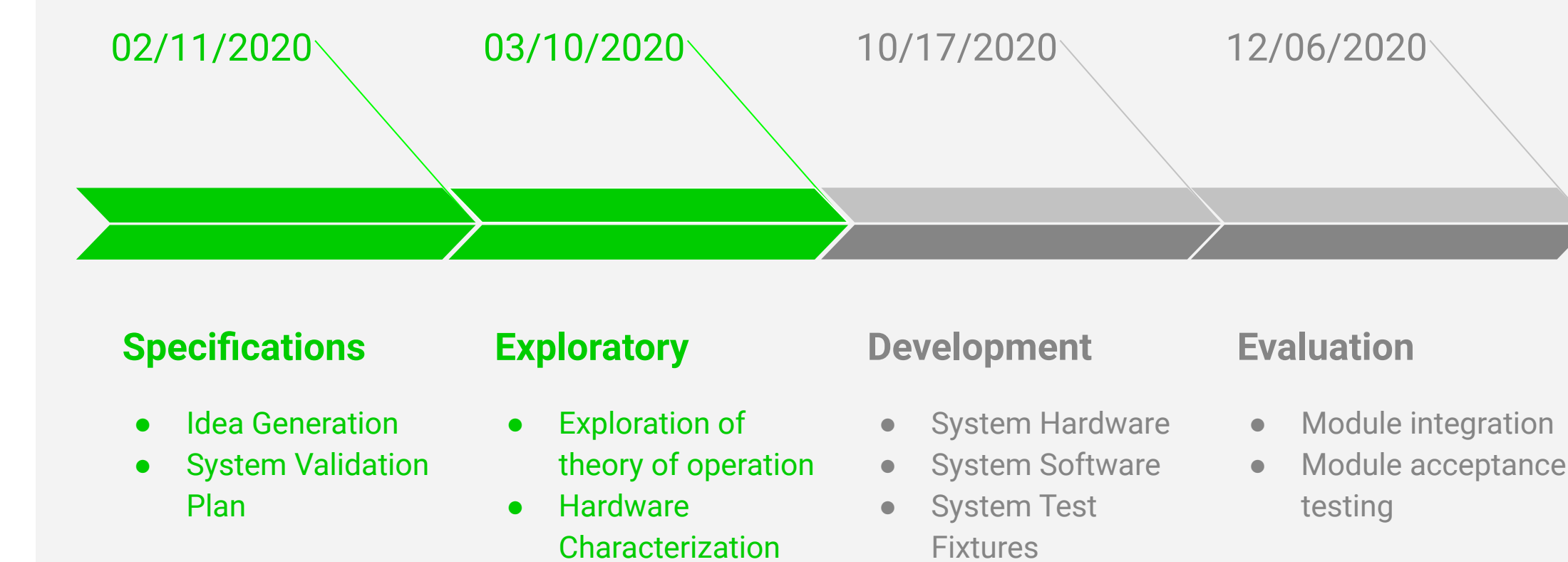
- Test transmit/receive hardware modules
- 3D print test rig and build test enclosure

### Software Tasks

- Implement software system

### Integration Tasks

- Construct & validate final prototype
- Deliver final prototype & test enclosure



## References

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